Specification

EPD-Module
Part-No. 70005802

5.4", 130 ppi, double sided driven, B/W

Revision 1.00

E-PI Confidential

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Editor	Stefan Ruckmich
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1. Introduction

1.1 Purpose of this document

This document describes the technical specification of the electrophoretic display module, part number 70005802. It defines general characteristics of the display module and the technical information required to integrate the display module into a potential product.

The 70005802 is an active-matrix electrophoretic display (AM EPD), with interface and a reference system design. The 5.4" active area contains 680 x 155 pixels. The module is a TFT-array driving electrophoretic display, with integrated circuits including gate driver, source driver and an integrated EPD-controller. The module can be used e.g. in portable electronic devices.

1.2 Key features of display module

- Consists of an active-matrix electrophoretic display driven chips combining source drivers, gate drivers and an integrated EPD-controller bonded on COFs (COF = chip on film)
- 680 x 155 pixels @ 196 µm pixel pitch
- Supports 16 grey levels
- Incorporates industry-leading bi-stable electrophoretic display technology in pure reflective mode and with ultra-wide viewing angle
- Low power consumption
- Enables robust product design (glass-free and flexible)
- The display has a pressure sensitive adhesive (PSA) on the backside
- Proper encapsulation has to be provided by the final product design

2. Mechanical specification

2.1 Dimensions

Parameter	Value	Unit	Comments
Active area size	5.4	inch	
Active area dimension	133.28 x 30.38	mm	
Active area resolution	680 x 155 130	Pixel ppi	
Active area pixel pitch	196	µm	
Outline length	149.50 ± 0.2	mm	~ 153.5mm with bent COFs
Outline width	36.00 ± 0.2	mm	
Thickness	0.50 ± 0.1	mm	without liner on backside PSA
FPC length	26.15 ± 0.3 30.75 ± 0.3	mm	length over display outline
Weight	4	g	typical with liners

Table 1: Mechanical specification

2.2 Outline

The outline of the final product is depicted in Figure 1.

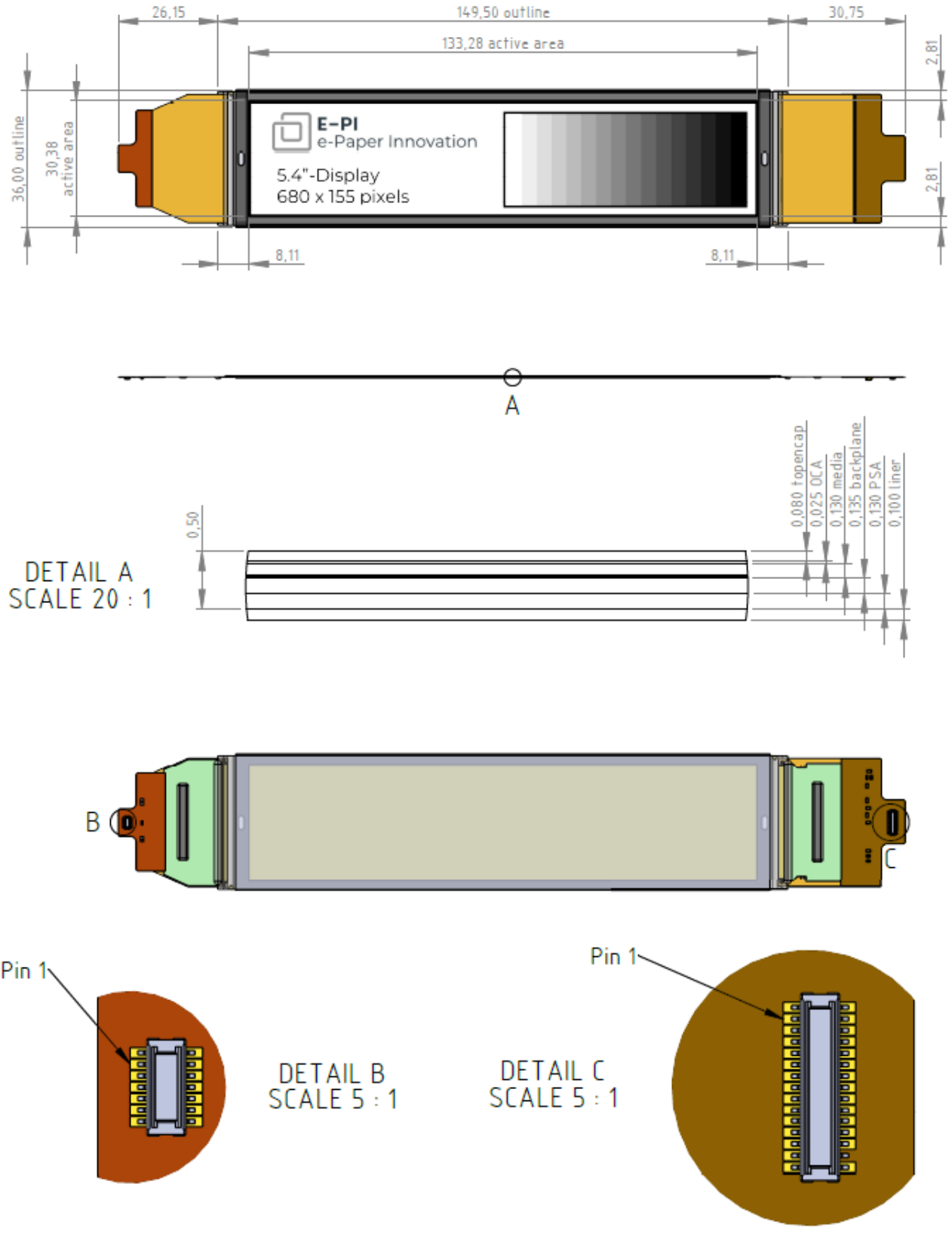

Figure 1: Dimensions of the final product in mm

3. Optical specification

The display supports 16 grey levels. Specific display grey levels are defined using the terminology GLx, where x = 0-15. GL0 corresponds to “black” and GL15 corresponds to “white”.

Grey levels are measured using the L* scale, which is the lightness component of the CIE 1976 CIELUV and CIELAB [2] colour spaces. Measurements in L* can be related to reflectivity using the formula $L^*=116(L/L_w)^{1/3}-16$, where L and Lw are measured in % reflectance and Lw is defined as 100 %. This formula is valid for L* > 16.

Measurements are made with illumination under an angle of 45 degrees, the detection is perpendicular unless otherwise specified, the viewing distance is 30 cm ± 10 cm, the ambient luminance is between 700 and 1000 Lux and ambient environmental conditions are temperature between 20°C and 25°C and humidity between 40%rH and 70%rH.

3.1 Optical requirements

3.1.1 Picture quality

The display will be lineout free; however typical expectations are a pixel yield of > 99.99 %.

3.1.2 Parameters for GL waveforms

Parameter	Description	Min	Typical	Max	Unit
White state	Median GL15	65	70	-	L*
Black state	Median GL0	-	24	28	L*

Table 2: Optical requirements

3.1.3 Display update waveforms

The display module will be supplied with a library of waveforms that are tuned to optimise display performance. The waveform library will be stored into the controller flash memory. Each waveform has an optimum temperature range over which it is valid – it is the responsibility of the display controller in combination with the temperature sensor to ensure that the correct waveform is selected for the current display module temperature.

E-PI will provide several waveforms. The update time is dependent on the waveform design. It might vary based on the number of grey levels and the expected optical performance (contrast, ghosting, etc.), see Table 3.

Waveform	Usage	Grey levels	Ghosting	Update speed
INIT	Recover from unknown state/ start up	white only	low	slow
GC16	Full refresh	16 GL	low	slow
DU	Delta Update; 16GL to Monochrome	16 GL → b/w	high	medium
A2	Fast monochrome updates	b/w → b/w	high	fast

Table 3: Waveform types

4. Reliability and other specifications

Parameter	Value	Unit	Comments
Operating Temperature	0 to 50	°C	The operating temperature of 0 to 50 °C is a limitation of the media material
Storage Temperature	-25 to 50	°C	
Operation/Storage humidity	25 to 60	%RH	T ≤ 40°C without dewing
Waterproofing	n/a		The deliverables of this product will not guarantee an IP rating

Note: long-term exposure of stress close to the limits might reduce the overall lifetime

Table 4: Reliability parameters

No.	Test	Condition	Comments
1	High Temperature Operation	T = +60°C, RH = 30 % for 240 hrs	IEC 60 068-2-2Bp
2	Low Temperature Operation	T = 0°C for 240 hrs (RWU-profile)	IEC 60 068-2-2Ab
3	High Temperature Storage	T = +70°C, RH = 23 % for 240 hrs	IEC 60 068-2-2Bp
4	Low Temperature Storage	T = -25°C for 240 hrs	IEC 60 068-2-1Ab
5	High Temperature, High Humidity Operation	T = +40°C, RH = 90 % for 360 hrs (RWU-profile)	IEC 60 068-2-3CA
6	High Temperature, High Humidity Storage	T = +60°C, RH = 90 % for 360hrs	IEC 60 068-2-3CA
7	Thermal Shock	Cycle: [-25°C/30min] → [+70°C/30 min] 100 cycles	IEC 60 068-2-14
8	Real World Use	T = +22°C for 500hrs T = +40°C for 500hrs	RWU-profile
9	Ambient Operation	T = +22°C for 1 Mio updates	1s image updates
10	Electrostatic Effect (nonoperating)	Machine model +/- 250 V, 0 Ω, 200 pF	IEC 62179, IEC 62180

Table 5: Reliability tests; at the end of the test, electrical, mechanical, and optical specifications shall be fulfilled

5. Electrical specification

5.1 Connection of display module

The display module can be contacted via mezanine connectors on both FPCs, see Figure 1. Recommended connector types are WP7A-S026VA1-R500 (26 pins) and P7A-S010VA1-R500 (10 pins).

5.2 Electrical pin-out of display module

Following tables list the pin-out and description of all signals of the display module.

No.	Name	Description
1	CLKI	EPDC clock
2	HIRQ	Interrupt line from EPDC
3	VSNEG	Negative HV supply for source driver (-15V typ)
4	VCC_3V3	3V3 power pin
5	VSPOS	Positive HV supply for source driver (+15V typ)
6	SPICLK	SPI clock for EPDC host interface
7	VBORDER	Supply for border area
8	SPI_MISO	SPI MISO for EPDC host interface
9	VCC_3V3	3V3 power pin
10	SPI_MOSI	SPI MOSI for EPDC host interface
11	GD_SP	
12	HRDY	
13	GD_CLK	
14	GND	Ground
15	GD_OE	
16	SPICS	SPI chip select for EPDC host interface
17	VCOM	Switched VCOM supply (tuned for each panel)
18	MP_RESET	EPDC power-on reset pin
19	VME2	
20	PWRSTAT	
21	VME1	
22	PWRCOM	
23	GD_RL	
24	PWR0	
25	GND	Ground
26	GND	Ground

Table 6: Electrical pin-out of source connector

No.	Name	Description
1	GD_UD	EPDC clock
2	VCC_3V3	3V3 power pin
3	VGPOS	Positive HV supply for gate driver (+25V typ)
4	GD_SP	
5	GND	Ground
6	GD_CLK	
7	VGNEG	Negative HV supply for gate driver (-32V typ)
8	GD_OE	SPI MISO for EPDC host interface
9	GND	Ground
10	VCOM	Switched VCOM supply (tuned for each panel)

Table 7: Electrical pin-out of gate connector

5.3 Controller

The display is driven and controlled by an UC8434 gate driver chip and the Epson S1D134541 EPD-controller with source drivers, display engine, display memory, waveform memory, command memory, multi time PROM and temperature sensing system.

Key features are

- Serial Interface (SPI) as host interface
- Transparency write
- Supported data formats: 1bpp, 2bpp, 4bpp, 1 Byte-per-pixel
- 202K Bytes image buffer
- Mirror and rotate mode (Vertical/Horizontal Mirror, 0°/90°/180°/270° Rotate)
- 16K Bytes embedded memory for waveform storage
- 16 or 32 MHz clock input
- Built-in thermistor sensing system (external thermistor)
- I2C serial master
- External gate driver control, type 1
- External power management IC control

Please refer to the EPSON S1D13541 datasheet for full controller specification.

The FPCs contain additional components required by the controller and the temperature sensor.

5.4 Display Power Supply System

The display module itself does not contain the power management subsystem required to generate the high voltages driving the display. Reference circuitry can be provided by E-Pi recommending either a Maxim MAX17135 or a TI TPS65185.

6. Handling requirements

The display modules are shipped in appropriate packages. All loose tails must be constrained during shipping.

Keep the EPD-panel/module in the specified environment and original packing boxes when stored to avoid scratching and to keep original performance.

The product is intended to be used around ambient temperature. Please control the temperature to avoid frost or dew on the surface of EPD-panel. Moisture may ingress the EPD-panel and may damage the EPD-panel.

High temperature, high humidity, sunlight, or UV-light may degrade the EPD-panel's performance. Please do not expose the unprotected EPD-panel to high temperature, high humidity, sunlight, or UV-light for long periods of time. Please store the EPD panel in controllable environment of warehouse and in original package, without sunlight, without condensation.

The display module should not be exposed to harmful gases, such as acid and alkali gases, which corrode the electronic components.

Do not disassemble or re-assemble the EPD-panel.

The EPD panel/module is manufactured from fragile materials such as glass and plastic and may be broken or cracked if dropped. Please handle with care. Do not apply force such as bending or twisting to the EPD panel.

Use a soft dry cloth without chemicals for cleaning. Please don't press hard for cleaning because the surface of the protection sheet film is very soft and without hard coating.

Minimise pulling or bending of the FPC and do not use the FPC for pick up. Do not press or pull FPC to ± 90° bending and don't twist FPC and PCB. Keep r ≥ 1mm when bending for assembly.

To avoid scratches, do not stack the EPD-panels/modules.

Do not apply pressure or hit to the EPD-module, IC, FPC and PCB to prevent damage.

Do not touch the FPC lead contacts and IC area to avoid ESD damage.

The display module is an electrostatic discharge (ESD) sensitive device. Ensure that proper ESD-precautions are in place to avoid damage of the display module.

Typical operation conditions in the assembly area are (25 ± 2)°C and 50%RH to 60%RH. Operators should use ionizer fan during assembly. The working area must be connected to ground, the resistance between operator and working area should be less than 5 Ω. Operator should wear PVC or anti-electrostatic gloves. Operators should wear grounding wrist ring and must make sure that grounding is fine. Before working in operation area, please ensure wrist ring and suit is fine by passing a conductive test before operation.

To avoid electrical overstress (EOS) in operation, please

- ensure that power is off before EPD is installed into jig/tag
- ensure that FPC is well connected (no misalign in connector) to avoid a short
- do not remove/uninstall EPD from jig/tag when image is updating,
- switch power off after operation or function test before uninstalling the EPD

EOS or short may damage the IC or the TFT.

7. Safety and flammability requirements

The integrator of this display module into a final product is responsible for ensuring that the relevant safety and flammability requirements are met.

8. Compatible evaluation kits

Do not use any other evaluation kit than the recommended one.